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** CONTINUING DATA *Yes DV*

This application is a CON of 10/379,543 03/06/2003 PAT 6,753,231

which is a CON of 10/157,978 05/31/2002 PAT 6,559,006

which is a CON of 09/372,007 08/11/1999 PAT 6,436,753

** FOREIGN APPLICATIONS *Yes DV*

JAPAN 10-226663 08/11/1998

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/18/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>Shuji Ikeda</i>	Examiner's Signature <i>DV</i> Initials	JAPAN	DRAWING 20	CLAIMS 10	CLAIMS 4

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TITLE

Semiconductor integrated circuit and method for manufacturing the same

All Fees

1.16 Fees (Filing)

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